



SEMINAR

Center for Advanced Research in Photonics
&
Department of Electronic Engineering
The Chinese University of Hong Kong

Friday, 30th November 2007
2.30pm-3.30pm
Rm 222 Ho Sin Hang Engineering Building,
The Chinese University of Hong Kong

The ePIXnet silicon photonics platform and research at IMEC Pieter Dumon

IMEC – Ghent University, Dept. of Information technology
Sint-Pietersnieuwstraat 41, 9000 Gent, Belgium
e-mail: Pieter.Dumon@imec.be

The ePIXnet silicon photonics platform offers open access to high-end CMOS facilities for research and prototyping of silicon photonic integrated circuits. Through standardization and extensive cost sharing, the cost of wafer scale large-scale integrated circuits becomes affordable. The generic integration technology allows for freedom in design while standardizing on the fabrication technology and physical specifications.

The platform will be presented, as well as the technological state of the art and several recent achievements based on this technology. The research at IMEC on silicon photonics will be presented, including passive devices and the combination of silicon with InP and other materials for active, non-linear and biosensing devices and applications.

Pieter Dumon obtained the Master in Electrical Engineering degree from Ghent University in 2002. He obtained the Doctorate in Electrical Engineering degree in 2007, also from Ghent University, with the PhD thesis “Ultra-Compact Integrated Optical Filters in Silicon-on-Insulator by Means of Wafer-Scale Technology”. Pieter now coordinates the ePIXnet silicon photonics platform, which gives access to the CMOS facilities at IMEC (B) and CEA-LETI (F) for wafer-scale research and prototyping of silicon photonic components. Pieter is a member of the IEEE and the OSA. He has authored and co-authored 22 publications in international journals and over 50 conference publications.

***** All are welcome to attend *****

For further information contact Prof.H.K.Tsang (hktsang@ee.cuhk.edu.hk)